

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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In re Patent Application of:  
Hiromichi INAISHI et al.

Application No.: 10/575,758

Confirmation No.: 5126

Filed: March 5, 2007

Art Unit: 2182

For: PRINTED CIRCUIT BOARD DESIGN  
INSTRUCTION SUPPORT METHOD AND  
DEVICE

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Examiner: S. C. Sun

**RESPONSE TO FINAL ACTION UNDER 37 C.F.R. 1.116**

**MS AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Madam:

**INTRODUCTORY COMMENTS**

In response to the Office Action dated February 5, 2010, the following remarks are respectfully submitted in connection with the above-identified application.

**This Reply includes:**

**Remarks; and**

**Declaration Under 37 C.F.R. § 1.131.**